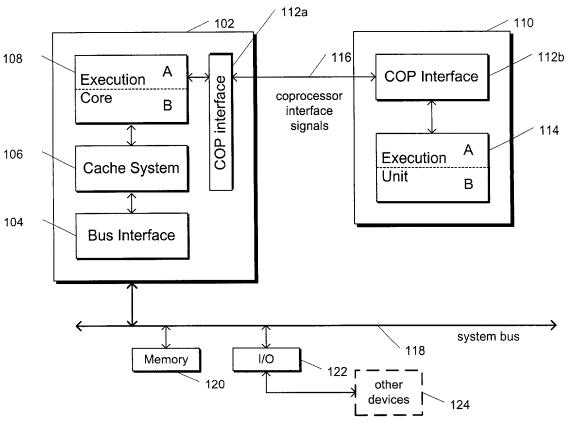
Figure 1 / 100 - 110



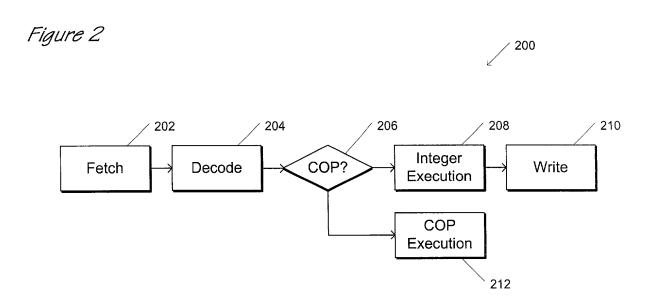
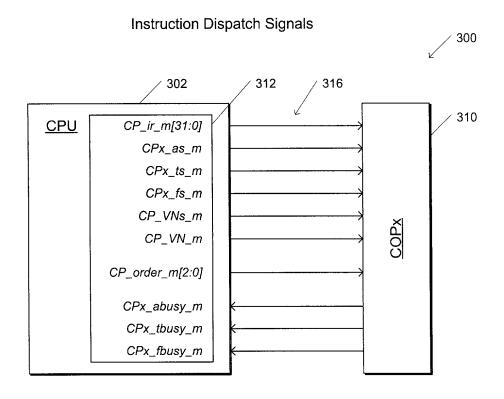


Figure 3



400

Figure 4

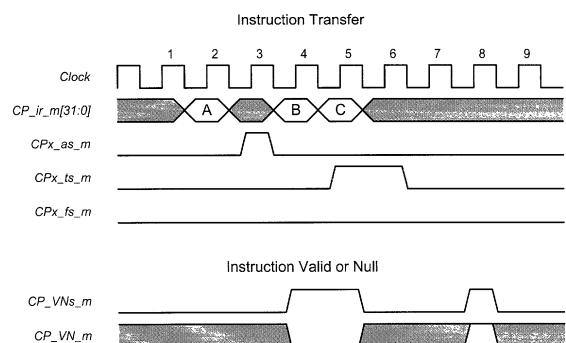


Figure 5

Instruction Transfer-Multiple Issue Groups

500

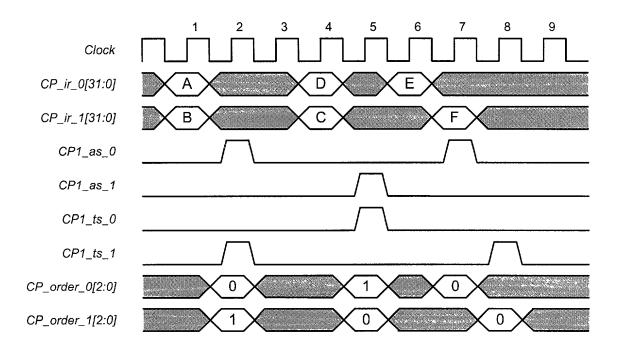


Figure 6
Instruction Transfer-COProcessor Busy

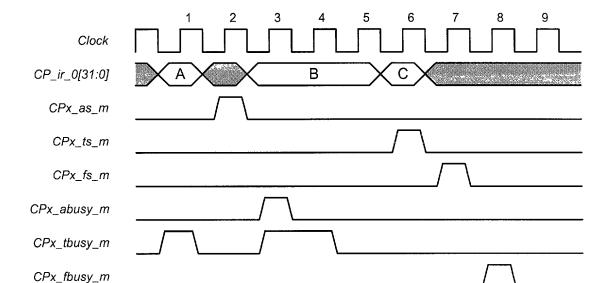
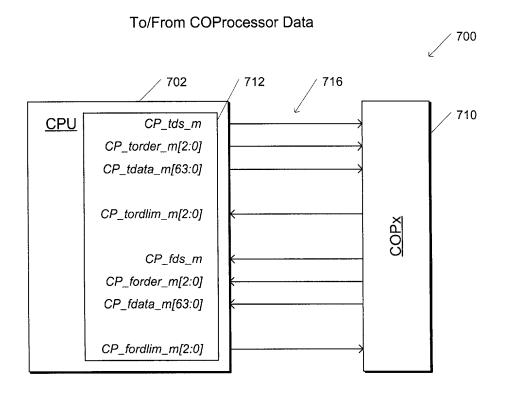
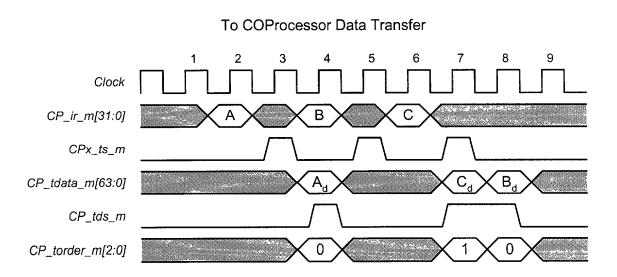


Figure 7



800

Figure 8



+

Figure 9

900

From COProcessor Data Transfer

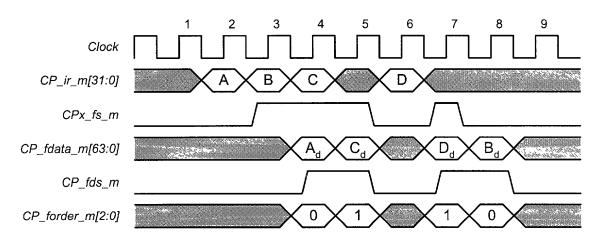


Figure 10

COProcessor Condition Code Check / GPR / exception Found / Instruction Commit

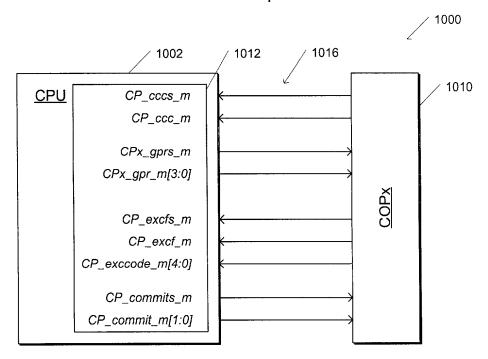




Figure 11

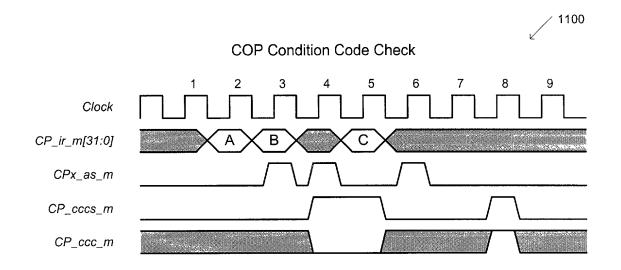
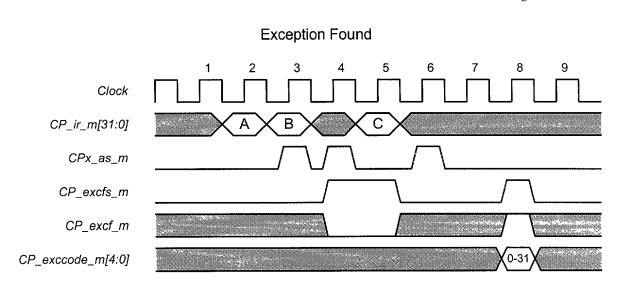


Figure 12



1200

+

Figure 13

